

A RISC-V Based SoC with Configurable CPK Sensor Interface for ECU on Motorcycle

Xinchao Zhong
School of Computer Science
The University of Auckland
Auckland, New Zealand
xzho323@aucklanduni.ac.nz

Chiu-Wing Sham
School of Computer Science
The University of Auckland
Auckland, New Zealand
b.sham@auckland.ac.nz

Abstract—A RISC-V based SoC (system on chip) with a configurable crankshaft position sensor (CPK) interface for an engine control unit (ECU) on a motorcycle is presented. The proposed SoC is the first SoC with RISC-V core for a motorcycle ECU, integrating a dedicated configurable engine crank position sensor interface to facilitate accurate and low-latency fuel injection and ignition control for the internal combustion engine on a motorcycle, while greatly reducing the computational capability demand for the MCU inside the ECU.

Index Terms—RISC-V, system on chip, SoC, crankshaft position sensor, CPK sensor, engine control unit, ECU

I. INTRODUCTION

An engine control unit (ECU) for motorcycles controls multiple systems of an internal combustion engine, such as the fuel injection system [1], [2], the ignition system [3] and the variable valve timing [4]. Among sensors used by ECU, the crankshaft position sensor is the most critical. It detects the position of the crankshaft for the ECU to control fuel injection and ignition [5]. Most ECUs use general microcontroller units (MCU)s and GPIO-based software for crankshaft position sensor (CPK) sensor signal processing. Because the ECU for motorcycles is price-sensitive, the MCU's performance is limited. The rotation speed can be as high as 12000rpm, therefore the computational load is typically high for the MCU. The performance of limited performance of the MCU decreases the positioning accuracy of the CPK sensor, thereby reducing the efficiency of the engine.

RISC-V is an open-source, license-free, modular, extensible instruction set architecture and is suitable for custom silicon chips. These advantages make RISC-V an ideal choice for applications that require in-depth optimization for performance, power, cost and security [6]. In addition, the enhancement of integrated solutions such as clock management [7], [8], placement [9], congestion [10] and routing [11] can reduce the time-to-market and development costs of SoC (System-on-Chip) solutions. The improving SoC solutions further enabled us to develop a SoC solution with the RISC-V.

While SoC solutions become more popular [12]–[14], RISC-V is one of the better choices for the customization of various applications [6], [15]. In this paper, we propose a RISC-V-based SoC for ECU on motorcycles. The main contributions of this study are as follows:

- To the best of our knowledge, this is the first RISC-V-based SoC for an ECU on a motorcycle. A dedicated

configurable CKP interface module is designed and integrated, which significantly improves the accuracy and reduces the latency and software workload.

- Major design considerations for the configurable deglitch module, single-tooth processing module and multi-tooth processing module in the CKP sensor interface are presented.

II. CONFIGURABLE CPK SENSOR INTERFACE DESIGN

Most CPK sensors are inductive, in which a magnetization core and a copper conductor winding are mounted on an isolated coil. The CPK sensor, cogwheel and the output of the sensor are shown in Fig. 1. The Configurable CPK sensor interface comprises a deglitch module, a single-tooth processing module (STPM), a multi-tooth processing module (MTPM) and other modules.

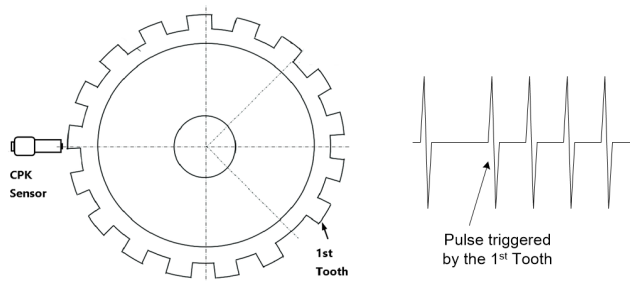


Fig. 1. CPK sensor and its output signal

A. Configurable Deglitch Module

The output of a crankshaft position sensor is analog and susceptible to interference; therefore, glitches are usually generated after this analog signal is converted to a digital signal. A configurable deglitch module is designed to filter out these glitches. To enhance its flexibility, the filter parameters can be configured through an inner-chip bus by the RISC-V core. Specifically, the proposed filter is designed to effectively filter out pulses up to 15 μ s. The output of the deglitch module goes to the STPM or MTPM.

To control injection and ignition, the position information of the crankshaft is crucial, which is usually referred to in terms of the angle in degrees. The software starts the injection and ignition at appropriate angles. The single-tooth processing module (STPM) and multi-tooth processing module (MTPM) obtain the output of the

configurable deglitch module, calculate the angles of the crankshaft, and generate interrupts to signal the software to take further action.

In low-cost motorcycles, only one tooth is present on the crankshaft cogwheel. When the tooth approaches the CKP sensor, a distinct signal is generated. This signal serves as an indicator of the initiation of a new round and is processed by the deglitch module and output to the STPM. Based on the number of clock cycles in the previous round, the STPM can detect the instant position and trigger interrupts, injections or ignitions when the positions meet the target positions configured by the software.

If the ECU enables auto-ignition and angle calculation features in the STM module, once the ECU configures the ignition timing parameters for the STPM, it can offload massive real-time calculations, leading to improved engine efficiency and performance. The features mentioned above are shown in Fig. 2.

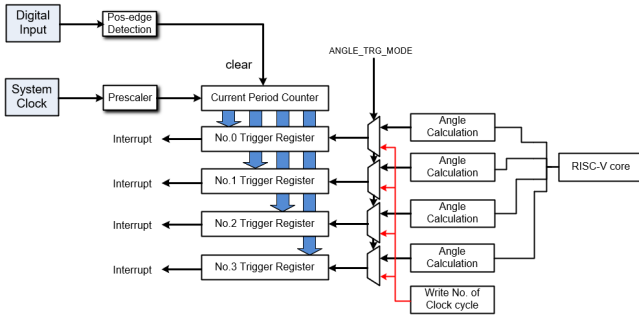


Fig. 2. STM triggering mechanism

B. Multi-Tooth Processing Module

The Multi-Tooth Processing Module (MTPM) is developed to process the CPK signals when the cogwheel has multiple teeth.

In the case of multiple teeth, the number of pulses generated by the CPK sensor in a round is the same as the number of teeth, thus providing more information for successive processing. More information demands more processing capabilities for the ECU. The MTPM implements most of the calculation and control functions for injection and ignition control, which greatly reduces the workload of the software.

The proposed MTPM also incorporates an automatic tooth number detection algorithm and control logic for multi-cylinder engines.

Compared to GPIO-based software processing, the proposed circuit-level STPM and MTPM achieve higher precision and much lower latency, as shown in the table below:

TABLE I
CHARACTERS OF TWO CPK SENSOR INTERFACE IMPLEMENTATIONS

	Proposed interface	GPIO-based emulated interface
Latency	<3 clock cycles	20 clock cycles or more
Cost	Extra logic	Higher MCU usage rate
Precision	High precision	Execution time may vary

III. CONCLUSION

A RISC-V-based SoC for ECU on motorcycles is presented. By integrating a dedicated configurable engine crank position sensor interface, the SoC can improve the accuracy and response speed of the engine control, while lowering the MCU cost by offloading most crank position detection and calculation tasks from the MCU.

For the engine crank position sensor interface, configurable digital deglitch logic is designed to filter out noise and interference. Single-tooth and multi-tooth processing modules are applied to a cogwheel with single and multiple teeth respectively. In both cases, the proposed CKP sensor interface receives and processes the position information accurately and effectively, achieving high quality with competitive price ECU design for motorcycles.

REFERENCES

- [1] E. V. Vijay, C. V. R. Rao, E. V. Kumar, and G. N. Swamy, "Electronic control unit for an adaptive cruise control system engine management system in a vehicle using electronic fuel injection," in *INTERACT-2010*, 2010, pp. 143–146.
- [2] P. Wang, F.-s. Liu, and X.-r. Li, "Driving characteristics for electronic control unit pump fuel injection system," in *2012 International Conference on Computer Distributed Control and Intelligent Environmental Monitoring*, 2012, pp. 264–267.
- [3] J. S. Potdar and Y. B. Mane, "Hardware design and development of engine control unit for four cylinder engine," in *2018 Fourth International Conference on Computing Communication Control and Automation (ICCUBEA)*, 2018, pp. 1–5.
- [4] R. Isermann, *Engine Modeling and Control: Modeling and Electronic Management of Internal Combustion Engines*. Springer, 2014.
- [5] L. S. Mendonça, D. D. Luceiro, M. E. S. Martins, and F. E. Bisogno, "Development of an engine control unit: Implementation of the architecture of tasks," in *2017 IEEE International Conference on Industrial Technology (ICIT)*, 2017, pp. 1142–1146.
- [6] X. Zhong, C.-W. Sham, and L. Ma, "A highly integrated risc-v based soc for on-board unit in etc system," in *2021 IEEE 10th Global Conference on Consumer Electronics (GCCE)*, 2021, pp. 302–303.
- [7] J. Lu, W.-K. Chow, and C.-W. Sham, "A new clock network synthesizer for modern vlsi designs," *Integration*, vol. 45, no. 2, pp. 121–131, 2012.
- [8] J. Lu, W.-K. Chow, C.-W. Sham, and E. F. Young, "A dual-mst approach for clock network synthesis," in *2010 15th Asia and South Pacific Design Automation Conference (ASP-DAC)*, 2010, pp. 467–473.
- [9] H. Zhou, C.-W. Sham, and H. Yao, "Revisiting routability-driven placement for analog and mixed-signal circuits," *ACM Trans. Des. Autom. Electron. Syst.*, vol. 23, no. 2, oct 2017.
- [10] C.-W. Sham, W.-C. Wong, and E. Young, "Congestion estimation with buffer planning in floorplan design," in *Proceedings 2002 Design, Automation and Test in Europe Conference and Exhibition*, 2002, pp. 696–701.
- [11] J. Lu and C.-W. Sham, "Lmgr: A low-memory global router with dynamic topology update and bending-aware optimum path search," in *International Symposium on Quality Electronic Design (ISQED)*, 2013, pp. 231–238.
- [12] X. Zhong and C.-W. Sham, "A visible light communication based integrated circuit for mobile payment," in *2019 IEEE 8th Global Conference on Consumer Electronics (GCCE)*, 2019, pp. 274–275.
- [13] X. Zhong, C.-W. Sham, H. Li, and Y. Li, "A 14-bit pulse-width modulation digital-to-analog converter for industrial sensors," in *2022 IEEE Asia Pacific Conference on Postgraduate Research in Microelectronics and Electronics (PrimeAsia)*, 2022, pp. 45–48.
- [14] X. Zhong and C.-W. Sham, "An soc for qi-compliant wireless power transmitter with enhanced emi performance," in *2019 6th International Conference on Systems and Informatics (ICSAI)*, 2019, pp. 725–728.
- [15] X. Zhong, C.-W. Sham, and L. Ma, "A risc-v soc for mobile payment based on visible light communication," in *2020 IEEE Asia Pacific Conference on Circuits and Systems (APCCAS)*, 2020, pp. 102–105.